IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner PATENT APPLICATION

Serial No.: 10/810,035 Group Art Unit: 2818

Filed: March 26, 2004 Examiner: T.T. Nguyen

Confirmation No.: 8458 Atty. Docket No.: ATM-291

For: NON-VOLATILE TRANSISTOR MEMORY ARRAY INCORPORATING

READ-ONLY ELEMENTS WITH SINGLE MASK SET

Amendment

Hon. Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir or Madam:

An Office action for the above-referenced application was mailed November 15, 2007. In response to the following amendments and remarks, a Notice of Allowance is requested.

Amendments to the drawings begin on page 2.

Amendments to the specification begin on page 3.

Amendments to the claims begin on page 5.

Remarks begin on page 11.

A Conclusion begins on page 13.

An Appendix including amended drawing figures is attached following page 13.

ATM83:291.AMT2